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To:	USPTO	From:	Jaison C. John
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No. of Pages:	Date: August 25, 2005		
Re:	RESPONSE TO OFFICE ACTION DATED MAY 26, 2005	File:	2008.001982

Urgent For Review Please Comment Please Reply Please Handle

* w/o coversheet

ORIGINAL: Will follow Will not follow

U.S. Patent Application Serial No. 10/712,150
 "Compression Circuit for Testing Memory Device"
 Inventors: Todd A. Merritt et al.
 Examiner: Joseph D. Torres
 Art Unit: 2133

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:	Group Art Unit:	2133
TODD A. MERRITT	Examiner:	JOSEPH D. TORRES
NICHOLAS VANHEEL	Conf. No.:	8235
Serial No.:	Atty. Dkt.:	2008.001982/98-0303.01 CUSTOMER NO.: 23720
Filed: NOVEMBER 13, 2003		
For: COMPRESSION CIRCUIT FOR TESTING MEMORY DEVICE		

RESPONSE TO OFFICE ACTION DATED MAY 26, 2005

MAIL STOP AMENDMENT
 Commissioner for Patents
 P.O. Box 1450
 Alexandria, VA 22313-1450

CERTIFICATE OF TRANSMISSION 37 C.F.R 1.8	
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Sir:

This paper is submitted in response to the Office Action mailed May 26, 2005, for which the shortened three-month statutory response date is August 26, 2005. Since this paper is being submitted on August 25, 2005, this paper is timely filed.

If an extension of time is required to enable this paper to be timely filed and there is no separate Petition for Extension of Time filed herewith, this paper is to be construed as also constituting a Petition for Extension of Time Under 37 CFR § 1.136(a) for a period of time sufficient to enable this document to be timely filed.

No fee is believed to be due in connection with the filing of this document. However, should any fee under 37 C.F.R. §§ 1.16 to 1.21 be deemed necessary for any reason relating to

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